IEEE Signal Processing Society

PRODUCT DEVELOPMENT METHODOLOGIES FOR SUCCESS! (DIGITAL SIGNAL PROCESSING SYSTEMS; IEEE PCJS DSP Ch Talk)

IEEE Signal Processing Society PCJS Chapter Meeting
DATE: Tuesday, Nov. 28, 2017; 6:55PM-8:45PM (talk 7:15-8:15)
LOC: Bridgewater Library, Mtg. Room B - NO REG REQ'D

ABSTRACT

Engineers can increase their ability to innovate, plan, and develop new products using DSP technologies that succeed in the marketplace by following product development methodologies, using current standards and practices, and continuing education. They can also facilitate success by understanding how marketing, engineering, and business financial planners work together in a company to plan products that address customer needs and expectations plus devise a schedule and budget that will lead to company profit.

The talk covers DSP Applications, circuit examples, DSP IC architectural features (multiplier/accumulators, Harvard architecture, L1 deterministic caches, DDR3 main memory, LVDS high speed I/O), the DSP marketplace, use of simulation tools to design software, HDL for FPGA's, ASIC's.

"Product Development Methodologies for Success!" - Digital Signal Processing Systems also covers:

- How to be innovative by planning continuing education on current technologies, standards, and practices to fuel creative thinking.
- Position your company for success by using your creative potential to plan new products that address customer needs and expectations well.
- Succeed at producing quality products on time by following a structured methodology to break the execution phase down into steps.
- Increase peer interaction to have fun and learn from others through formal and informal peer discussion and review.

Date: 28 November 2017
Time: 06:45 PM to 08:45 PM
Location: Bridgewater NJ Library
Bridgewater Library Phone Number: 908-526-4016
    1 Vogt Road
    Bridgewater, New Jersey
    United States 08807
    Building: Somerset County Library System, Bridgewater Branch
    Room Number: Meeting Room B
    [Click here for Map](#)

Speaker

[JERRY BELLOTT](#) of IEEE PCJS SPS Chapter Vice Chair

Topic:
PRODUCT DEVELOPMENT METHODOLOGIES FOR SUCCESS! -
SIGNAL PROCESSING SYSTEMS

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Agenda
6:55PM - Arrival, networking, light refreshments
7:15-8:15PM - Talk by Jerry Bellott, MSEE IEEE PCJS SPS Chapter Vice Chair
8:15-8:45PM - Questions, Networking, Light Refreshments

Jerry Bellott is an electrical engineer with more than 30 years experience designing products for the PBX, data networking, broadband switch, DSP multi-processing and computing industries. Mr. Bellott was on the team that designed the first truly compact 2G digital cell phone. In 2004, Mr. Bellott co-designed a cPCI board using

the 1GHz, 64 core MathStar DSP for Valley Tech. He has an MSEE from Georgia Tech (1980), and a BSEE from WVU (1979).