



## IEEE PCJS Section Meeting

Monday, March 28<sup>th</sup>, 6:30PM – 8:30PM - Non-members Welcome

### Talk, “System Design and Quality Management” by Jerry Bellott, MSEE

On Tuesday, March 28, 2011 at 7PM, the IEEE PCJS Section PACE and GOLD Chapters and DeVry University will host a presentation titled “System Hardware and Software Design and Enterprise Quality Management” by Jerry Bellott, MSEE.

#### About the Talk

Electrical Engineers can increase their ability to innovate, plan, and develop new products that succeed in the marketplace by following product development methodologies, using current standards and practices, and continuing education. They can also facilitate success by understanding how marketing, engineering, and business financial planners work together to plan products that address customer needs and expectations plus devise a schedule and budget that will lead to company profit.

The System Hardware and Software Design and Enterprise Quality Management seminar covers:

- An intro to product development methodologies and the role of marketing and business planners
- Engineering milestones as scheduling tools
- Hierarchical organization of engineering system requirements and specifications for new products.
- Common tools used for schematic capture, custom device design and simulation, and software development.
- Unit and system validation steps
- Test plans with traceability to marketing or custom contract product requirements.
- Automated project management tools that aid test traceability to product requirements.
- Enterprise tools used to help managers track the status of multiple projects, with the ability to identify percentage completion of milestones, to assess risks, and reorganize schedule priorities.

#### About the Speaker

Jerry Bellott is an electrical engineer with more than 25 years experience designing products using processors, microcontrollers, DSP's, analog I/O and developing telecom and networking interfaces, plus 2G and 3G cellular products at AT&T and Lucent Technologies' Bell Labs and other companies.

In 2000, Mr. Bellott was senior systems engineer at startup ViaGate Technologies where he co-developed the first “fiber-to-the-basement” ATM switch with SONET fiber and VDSL interfaces. The switch provides digital video, internet access, and remote VLAN access to up to 240 clients. ViaGate became part of another company that is now part of Motorola, Inc. More recently, Mr. Bellott co-designed a 64-mac DSP circuit board using a MathStar IC which served as MathStar's primary customer IC demo and evaluation platform for a two year period. MathStar DSP's contain 64 multiply/accumulate circuits that run at 1 gigahertz each. Mr. Bellott has since designed test plans and written project documentation for PC, PowerPC, LAN and fibre channel SAN equipment, and application GUI's for DSP analysis at a central NJ technology company. He has also written documentation for microwave HD video links and streaming video over LAN products at IMT, Inc. Mr. Bellott currently delivers talks on design topics and has designed websites for business.

Mr. Bellott has an MSEE from Georgia Tech (1980) and a BSEE from West Virginia University (1979). Mr. Bellott served as Vice Chair of the Princeton Chapter of the IEEE Signal Processing Society from 2008 to 2011.

#### All Welcome!

You do not have to be a member of the IEEE to attend. Bring your friends and network.

**Time:** 6:30PM, Monday March 28, 2011 (talk is from 7-8 PM).

**Place:** DeVry University – Room 1F-210

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